



N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY			
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	
60	2.5 at V _{GS} = 10 V	0.25	
	3 at V _{GS} = 4.5 V	0.23	
	8 at V _{GS} = 3 V	0.05	

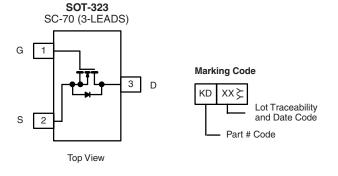
FEATURES • Halogen-fre

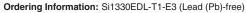
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- ESD Protected: 2000 V
- Compliant to RoHS Directive 2002/95/EC



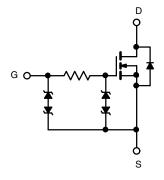


- · P-Channel Driver
 - Notebook PC
 - Servers





Si1330EDL-T1-GE3 (Lead (Pb)-free and Halogen-free)



ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	60		V	
Gate-Source Voltage		V _{GS}	± 20			
Continuous Drain Current (T, I = 150 °C) ^a	T _A = 25 °C	- I _D	0.25	0.24		
Continuous Diam Current (1j = 150 °C)	T _A = 70 °C		0.2	0.19	Α	
Pulsed Drain Current		I _{DM}	1.0		A	
Continuous Source Current (Diode Conduction) ^a		I _S	0.26	0.23		
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	0.31	0.28	W	
Maximum rower Dissipation	T _A = 70 °C		0.20	0.18		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Mariana Landia La Andria da	t ≤ 5 s	R _{thJA}	355	400	°C/W
Maximum Junction-to-Ambient ^a	Steady State		380	450	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	285	340	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

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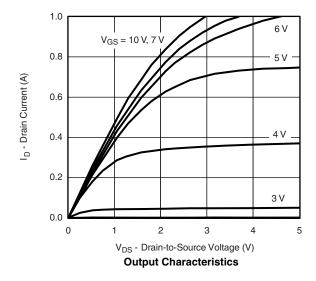
SPECIFICATIONS T _J = 25 °C, unless otherwise noted ^a							
			Limits				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 10 \mu\text{A}$	60			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1	2.0	2.5	'	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 10 \text{ V}$			± 1		
Zara Cata Valtaga Drain Current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	V _{DS} = 60 V, V _{GS} = 0 V		1	μΑ	
Zero Gate Voltage Drain Current		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	1	
		V _{GS} = 10 V, V _{DS} = 7.5 V	0.5				
On-State Drain Current ^b	I _{D(on)}	V _{GS} = 4.5 V, V _{DS} = 10 V	0.4			Α	
		V _{GS} = 3 V, V _{DS} = 10 V	0.05				
		V _{GS} = 10 V, I _D = 0.25 A		1.0	2.5		
Drain-Source On-Resistance ^b	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 0.2 \text{ A}$		1.4	3	Ω	
		$V_{GS} = 3 \text{ V}, I_D = 0.025 \text{ A}$		3.0	8		
Forward Transconductance ^b	9 _{fs}	V _{DS} = 10 V, I _D = 0.25 A		350		mS	
Diode Forward Voltage	V_{SD}	I _S = 0.23 A, V _{GS} = 0 V		0.83	1.2	V	
Dynamic ^b				•	•		
Total Gate Charge	Qg			0.4	0.6		
Gate-Source Charge	Q _{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}$ $I_{D} \cong 0.25 \text{ A}$		0.11		nC	
Gate-Drain Charge	Q _{gd}	1D = 0.23 A		0.15			
Gate Resistance	R _g			173		Ω	
Turn On Time	t _{d(on)}			3.8	10		
Turn-On Time	t _r	$V_{DD} = 30 \text{ V}, R_L = 150 \Omega$ $I_D \cong 0.2 \text{ A}, V_{GEN} = 10 \text{ V}$		4.8	15		
Time Off Time	l ta(off)			12.8	20	ns	
Turn-Off Time	t _f	y -		9.6	15	-	

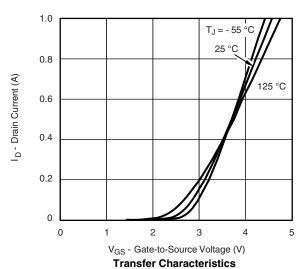
Notes:

- a. Pulse test: PW $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

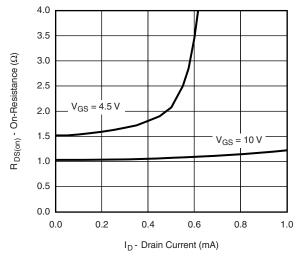




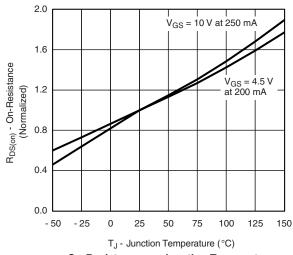




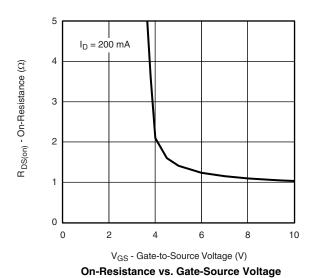
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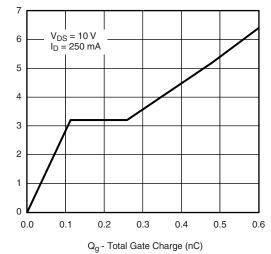
On-Resistance vs. Drain Current



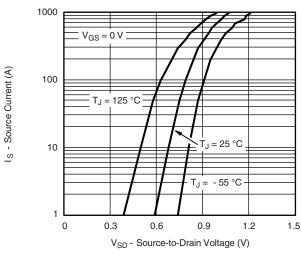
On-Resistance vs. Junction Temperature



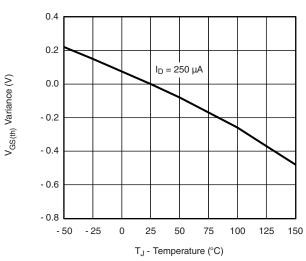
V_{GS} - Gate-to-Source Voltage (V)



Gate Charge



Source-Drain Diode Forward Voltage

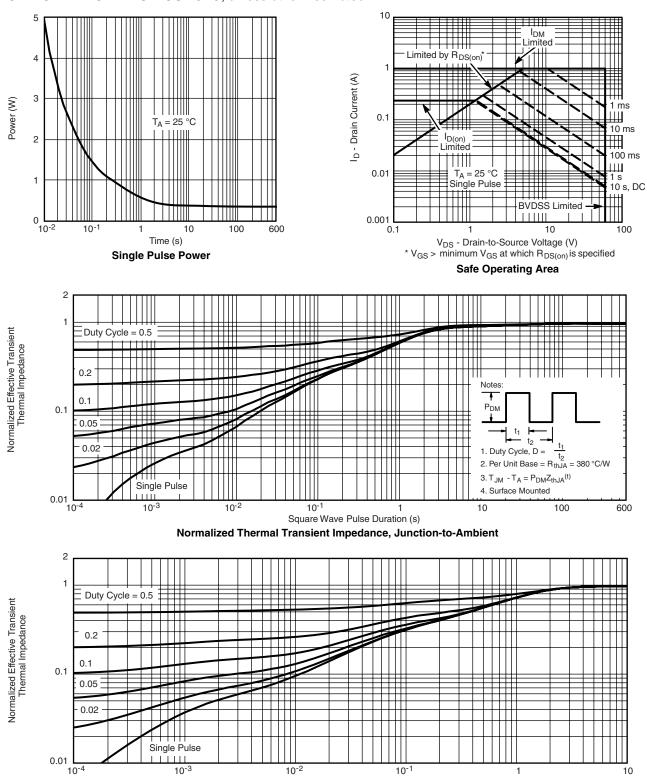


Threshold Voltage Variance over Temperature

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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Square Wave Pulse Duration (s)

Normalized Thermal Transient Impedance, Junction-to-Foot



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