

PN5432 **PN5433 PN5434**



N-Channel Switch

This device is designed for analog or digital switching applications where very low On Resistance is mandatory. Sourced from Process 58. See J108 for characteristics.

Absolute Maximum Ratings*

TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V_{DG}	Drain-Gate Voltage	25	V	
V _{GS}	Gate-Source Voltage	-25	V	
I _{GF}	Forward Gate Current	10	mA	
T _J , T _{stg}	Operating and Storage Junction Temperature Range	and Storage Junction Temperature Range -55 to +150 °C		

^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

These ratings are based on a maximum junction temperature of 150 degrees C.
 These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics

TA = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units
		PN5432 / 5433 / 5434	
P _D	Total Device Dissipation	350	mW
	Derate above 25°C	2.8	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	°C/W

N-Channel Switch (continued)

Symbol	Parameter	Test Conditions	Min	Max	Units
	RACTERISTICS				
$V_{(BR)GSS}$	Gate-Source Breakdown Voltage	$I_G = 1.0 \mu A, V_{DS} = 0$	-25		V
I_{GSS}	Gate Reverse Current	$V_{GS} = 15 \text{ V}, V_{DS} = 0$		-200 -200	pA nA
I _{D(off)}	Drain Cutoff Leakage Voltage	$V_{GS} = 15 \text{ V}, V_{DS} = 0, T_A = 150 ^{\circ}\text{C}$ $V_{GS} = 10 \text{ V}, V_{DS} = 5.0 \text{ V}$		-200	pА
	Train catch Loanage vehage	$V_{GS} = 10 \text{ V}, V_{DS} = 5.0 \text{ V},$			ρ, .
.,	0	T _A = 150 °C		-200	nA
$V_{GS(off)}$	Gate-Source Cutoff Voltage	$V_{DS} = 5.0 \text{ V}, I_D = 3.0 \text{ nA}$ 5432 5433	-	-10 -9.0	V
		5434		-4.0	V
ON OLIA 5	ACTEDIOTICS				
	RACTERISTICS	TV 45VV 0 540	150		A
I _{DSS}	Zero-Gate Voltage Drain Current*	V _{DS} = 15 V, V _{GS} = 0 543 543			mA mA
		543	-		mA
V _{DS(on)}	Drain-Source On Voltage	$I_D = 10 \text{ mA}, V_{GS} = 0$ 543		50	mV
()		543	-	70	mV
		543		100	mV
r _{DS(on)}	Drain-Source On Resistance	$I_D = 10 \text{ mA}, V_{GS} = 0$ 543 543	_	5.0	Ω
		543	-	7.0 10	Ω
		$I_D = 0$, $V_{GS} = 0$, $f = 1.0 \text{ kHz}$	`		32
		543		5.0	Ω
		543		7.0	Ω
		543	4	10	Ω
SMALL S	IGNAL CHARACTERISTICS				
C _{iss}	Input Capacitance	$V_{DS} = 0$, $V_{GS} = 10$ V, $f = 1.0$ MHz		30	pF
C_{rss}	Reverse Transfer Capacitance	$V_{DS} = 0$, $V_{GS} = 10 \text{ V}$, $f = 1.0 \text{ MHz}$		15	pF
SWITCHIN	NG CHARACTERISTICS				
t _d	Delay Time	$V_{DD} = 1.5 \text{ V}, \ V_{GS(on)} = 0,$		4.0	ns
	Rise Time	$V_{DD} = 1.5 \text{ V}, V_{GS(on)} = 0,$ $I_{D(on)} = 10 \text{ mA}$		1.0	ns
t _r	Storage Time			1.0	113
ts	Storage Time	$V_{GS(off)} = 12 \text{ V},$ $V_{DS(on)} = 50 \text{ mV}$ 543	2	6.0	ns
	1	I v DS(on) = 50 III V 343	4		
			3	6.0	ns
		$V_{DS(on)} = 70 \text{ mV}$ 543 $V_{DS(on)} = 100 \text{ mV}$ 543		6.0 6.0	ns ns

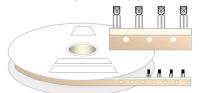
^{*}Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%

TO-92 Tape and Reel Data FAIRCHILD SEMICONDUCTOR TM **TO-92 Packaging** Configuration: Figure 1.0 **TAPE and REEL OPTION** FSCINT Label sample See Fig 2.0 for various Reeling Styles CBVK//418019 **FSCINT** Label 5 Reels per Intermediate Box Customized F63TNR Label sample Label F63TNR LOT: CBVK741B019 QTY: 2000 FSID: PN222N Customized QTY1: QTY2: Label 375mm x 267mm x 375mm Intermediate Box TO-92 TNR/AMMO PACKING INFROMATION **AMMO PACK OPTION** See Fig 3.0 for 2 Ammo Packing Style Quantity EOL code **Pack Options** 2,000 D26Z Е 2,000 D27Z Ammo М 2,000 D74Z D75Z 2,000 **FSCINT** Unit weight = 0.22 gm Reel weight with components = 1.04 kg Ammo weight with components = 1.02 kg Max quantity per intermediate box = 10,000 units Label 5 Ammo boxes per Intermediate Box 327mm x 158mm x 135mm Immediate Box Customized F63TNR Customized Label Label 333mm x 231mm x 183mm Intermediate Box (TO-92) BULK PACKING INFORMATION **BULK OPTION** See Bulk Packing DESCRIPTION QUANTITY Information table J18Z TO-18 OPTION STD 2.0 K / BOX Anti-static Bubble Sheets TO-5 OPTION STD NO LEAD CLIP 1.5 K / BOX J05Z **FSCINT Label** NO EOL TO-92 STANDARD STRAIGHT FOR: PKG 92, NO LEADCLIP 2.0 K / BOX 94 (NON PROELECTRON SERIES), 96 TO-92 STANDARD STRAIGHT FOR: PKG 94 (PROELECTRON SERIES BCXXX, BFXXX, BSRXXX), 97, 98 L34Z NO LEADCLIP 2.0 K / BOX 2000 units per 114mm x 102mm x 51mm EO70 box for std option Immediate Box 5 EO70 boxes per intermediate Box 530mm x 130mm x 83mm Customized Intermediate box Label FSCINT Label 10,000 units maximum per intermediate box for std option

TO-92 Tape and Reel Data, continued

TO-92 Reeling Style Configuration: Figure 2.0

Machine Option "A" (H)

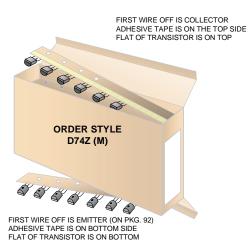


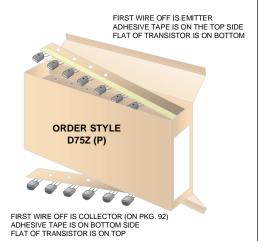
Style "A", D26Z, D70Z (s/h)

Machine Option "E" (J)

Style "E", D27Z, D71Z (s/h)

TO-92 Radial Ammo Packaging Configuration: Figure 3.0



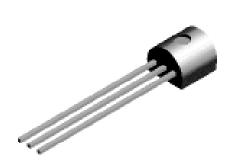


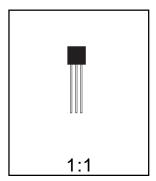


TO-92 Package Dimensions



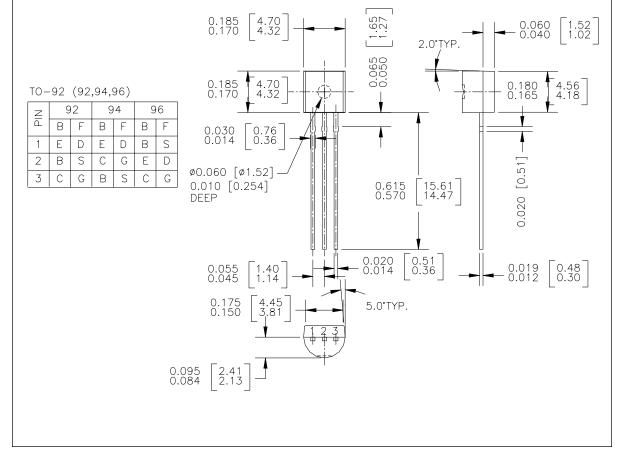
TO-92 (FS PKG Code 92, 94, 96)





Scale 1:1 on letter size paper
Dimensions shown below are in:
inches [millimeters]

Part Weight per unit (gram): 0.1977



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